

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Applicant:</b>	Tetsuya Ootsuki	<b>Examiner:</b>	Unassigned
<b>Serial No:</b>	Unassigned	<b>Art Unit:</b>	Unassigned
<b>Filed:</b>	Herewith	<b>Docket:</b>	17262
<b>For:</b>	SEMICONDUCTOR MEMORY DEVICE ADAPTIVE FOR USE CIRCUMSTANCE		
		<b>Dated:</b>	November 14, 2003


Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**CLAIM OF PRIORITY**

Sir:

Applicant in the above-identified application hereby claims the right of priority in connection with Title 35 U.S.C. § 119 and in support thereof, will submit in due course a certified copy of Japanese Patent Application No. 2002-332826, filed on November 15, 2002.

Respectfully submitted,

  
Paul J. Esatto, Jr.  
Registration No.: 30,749

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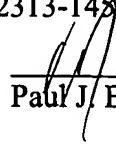
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Dated: November 14, 2003

  
Paul J. Esatto, Jr.